

Interview Summary	Application No.	Applicant(s)	
	09/737,218	NORMAN ET AL.	
	Examiner	Art Unit	
	B. James Peikari	2186	

All participants (applicant, applicant's representative, PTO personnel):

(1) B. James Peikari (PTO). (3)_____.

(2) David C. Peterson (#47,857). (4)_____.

Date of Interview: 02 November 2004.

Type: a) ☒ Telephonic b) ☐ Video Conference
c) ☐ Personal [copy given to: 1) ☐ applicant 2) ☐ applicant's representative]

Exhibit shown or demonstration conducted: d) ☐ Yes e) ☒ No.
If Yes, brief description: _____.

Claim(s) discussed: All pending claims.

Identification of prior art discussed: Yamada et al. (U.S. 5,617,537).

Agreement with respect to the claims f) ☒ was reached. g) ☐ was not reached. h) ☐ N/A.

Substance of Interview including description of the general nature of what was agreed to if an agreement was reached, or any other comments: Applicant agreed to email a proposed amendment in accordance with the examiner's suggestions to put the application in condition for allowance. If the amendment is approved by the examiner, the examiner will attach the emailed amendment to an examiner's amendment in order to have the changes formally entered.

(A fuller description, if necessary, and a copy of the amendments which the examiner agreed would render the claims allowable, if available, must be attached. Also, where no copy of the amendments that would render the claims allowable is available, a summary thereof must be attached.)

THE FORMAL WRITTEN REPLY TO THE LAST OFFICE ACTION MUST INCLUDE THE SUBSTANCE OF THE INTERVIEW. (See MPEP Section 713.04). If a reply to the last Office action has already been filed, APPLICANT IS GIVEN ONE MONTH FROM THIS INTERVIEW DATE, OR THE MAILING DATE OF THIS INTERVIEW SUMMARY FORM, WHICHEVER IS LATER, TO FILE A STATEMENT OF THE SUBSTANCE OF THE INTERVIEW. See Summary of Record of Interview requirements on reverse side or on attached sheet.

Examiner Note: You must sign this form unless it is an Attachment to a signed Office action.

Examiner's signature, if required

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record

A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews

Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.

All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:

- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:

- 1) A brief description of the nature of any exhibit shown or any demonstration conducted,
- 2) an identification of the claims discussed,
- 3) an identification of the specific prior art discussed,
- 4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
- 5) a brief identification of the general thrust of the principal arguments presented to the examiner,
(The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
- 6) a general indication of any other pertinent matters discussed, and
- 7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

S/N 09/737,218

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Robert D. Norman	Examiner:	James Peikari
Serial No.:	09/737,218	Group Art Unit:	2186
Filed:	December 14, 2000	Docket No.:	703.070US2
Title:	SYSTEM AND METHOD FOR ASSIGNING ADDRESSES TO MEMORY DEVICES		

PROPOSED AMENDMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Please amend the above-identified patent application as follows.

IN THE CLAIMS

Please amend the claims as follows:

1. (Previously Presented) A memory system comprising:
a plurality of memory devices associated with only one processor, with each memory device comprising:
 - (a) an array of memory cells;
 - (b) an addressing circuitry operatively coupled to the array of the memory cells, wherein the addressing circuitry is capable of providing addresses to the array of memory cells;
 - (c) a memory device bus interface;
 - (d) a command decoder which decodes commands at the memory device bus interface, including an address assign command; and
 - (e) a local address storage circuitry which stores a local address for identifying the storage circuitry's single associated memory device once the address assign command is decoded by the command decoder; and
a memory controller having a controller bus interface coupled to the memory device bus interface, with the memory controller providing the local address to be stored in the local address storage circuitry of the memory device of the memory system together with the address assign command.
2. (Previously Presented) The memory system of claim 1, wherein the controller bus interface of the memory controller is coupled to the memory device bus interface of the memory device by a system bus.
3. (Original) The memory system of claim 2, including a plurality of the memory devices wherein the memory controller transfers the local address to the memory devices over the system bus and the address assign command over the system bus.
4. - 63. (Canceled)

64. (Currently Amended) A memory system comprising:

a processor;

a memory controller;

~~a plurality of memory devices associated with only one processor; and~~

a plurality of flash memory devices associated with only one processor, with each memory device comprising:

(a) an array of memory cells;

(b) an addressing circuitry operatively coupled to the array of the memory cells, wherein the addressing circuitry is capable of providing addresses to the array of memory cells;

(c) a memory device bus interface;

(d) a command decoder which decodes commands at the memory device bus interface, including an address assign command;

(e) local address storage circuitry on each of the plurality of memory devices, wherein the local address storage circuitry is used to store a local address assigned from the memory controller that identifies a single associated memory device.

65. (Previously Presented) The memory system of claim 64, wherein the memory controller is configured to assign local addresses to each of the plurality of memory devices in a serial order.

66. (Previously Presented) The memory system of claim 64, wherein the memory controller includes an ASIC controller.

67. (Canceled)

68. (Currently Amended) A memory system comprising:

a processor;

[[a]] an ASIC memory controller;

~~a plurality of memory devices associated with only one processor;~~

a plurality of memory devices associated with only one processor, with each memory device comprising:

(a) an array of memory cells;

(b) an addressing circuitry operatively coupled to the array of the memory cells, wherein the addressing circuitry is capable of providing addresses to the array of memory cells;

(c) a memory device bus interface;

(d) a command decoder which decodes commands at the memory device bus interface, including an address assign command;

(e) local address storage circuitry on each of the plurality of memory devices, wherein the local address storage circuitry is used to store a local address assigned from the memory controller that identifies a single associated memory device; and

a system bus coupled between the memory controller and the plurality of memory devices to transfer the local address.

69. (Canceled)

70. (Currently Amended) The memory system of claim 68, wherein the a plurality of memory devices include a plurality of flash memory devices.

71. (Previously Presented) A memory system comprising:

at least one processor;

a memory controller;

a plurality of memory devices associated with only one processor, each memory device being connected to the memory system in a memory expansion socket; and, with each memory device comprising:

(a) an array of memory cells;

(b) an addressing circuitry operatively coupled to the array of the memory cells, wherein the addressing circuitry is capable of providing addresses to the array of memory cells;

(c) a memory device bus interface;

(d) a command decoder which decodes commands at the memory device bus interface, including an address assign command;

(e) local address storage circuitry on each of the plurality of memory devices, wherein the local address storage circuitry is used to store a local address assigned from the memory controller that identifies a single associated memory device.

72. (Previously Presented) The memory system of claim 71, wherein the memory controller includes an ASIC controller.

73. (Previously Presented) The memory system of claim 71, wherein the a plurality of memory devices include a plurality of flash memory devices.

74. (Previously Presented) The memory system of claim 71, further including a second processor and a plurality of memory devices associated with only the second processor.

REMARKS

An interview with the Examiner was conducted on November 11, 2004. Claims 1-3 were indicated as in condition for allowance. The remaining claims 64-74 were also discussed.

Pursuant to the interview, the amendments detailed above are proposed.

Claims 64, 68, and 71 are amended and claims 67 and 69 are canceled; as a result, claims 1-3, 64-66, 68, and 70-74 are now pending in this application.

Allowable Subject Matter

Claims 1-3 were indicated to be allowable. Applicant acknowledges and thanks the Examiner for the indication of allowance.

Conclusion

Applicant respectfully submits that the claims are in condition for allowance, and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 373-6944 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

ROBERT D. NORMAN

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6944

Date _____

By _____

David C. Peterson

Reg. No. 47,857

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this _____ day of November, 2004.

Name

Signature